

ABSTRACT

An active-matrix display apparatus is disclosed that includes: a substrate; an electrode for provision of a display element included in each of a plurality of pixels provided on the substrate; a first transistor Q2 that is connected to the electrode and a current-source line I_S (m); a second transistor Q1 that is connected to the gate of the first transistor Q2 and a data-storing signal line Data (m); a storage capacitor C1 connected to the current-source line I_S (m) and the gate of the first transistor Q2; and a third transistor Q_t connected to the electrode and to a gate signal line Gate (n-1) for another display element adjacent to the display element to lead current flowing from the first transistor to the electrode to the gate signal line Gate (n-1) for the other display element.